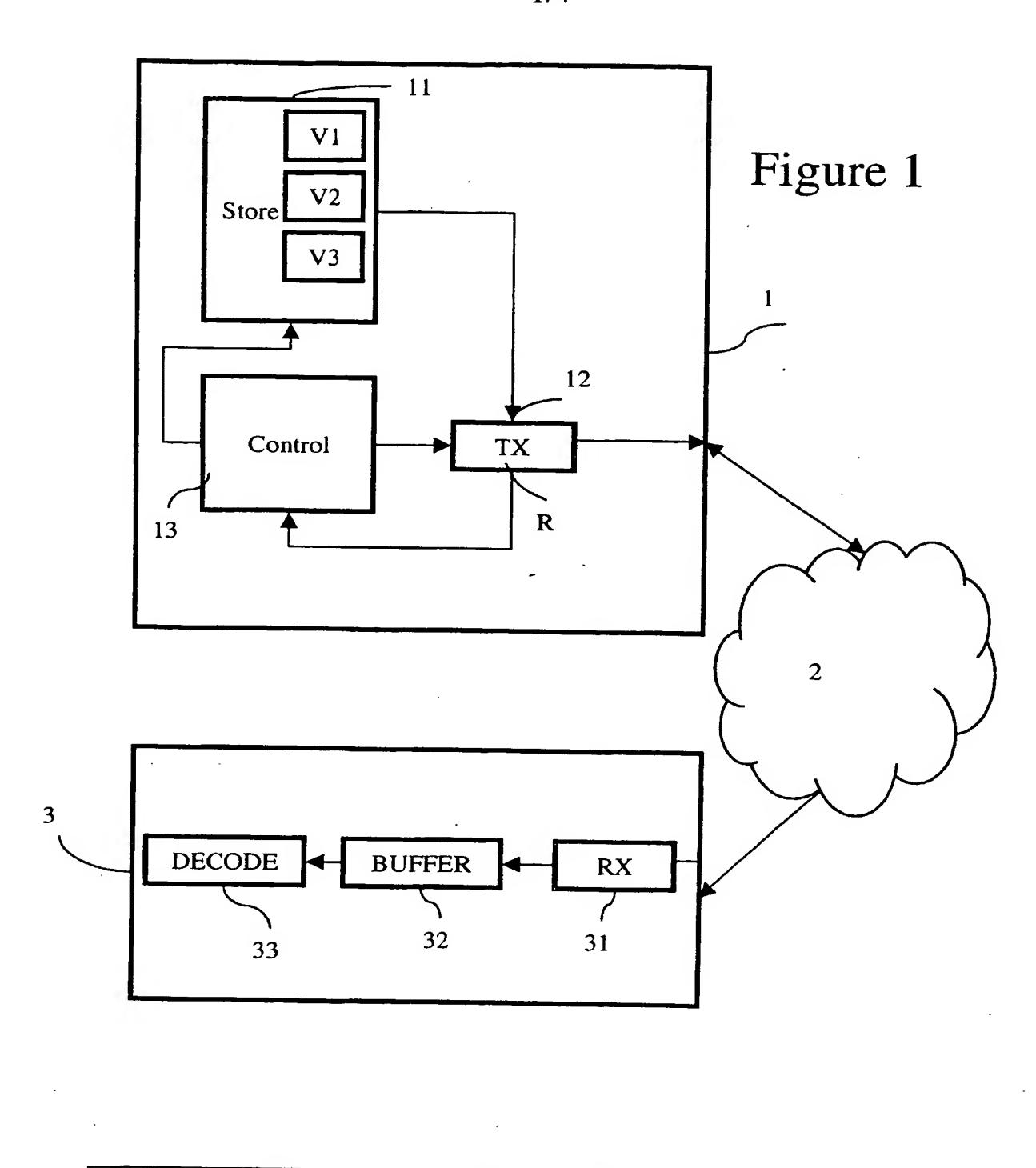
b₀ bits

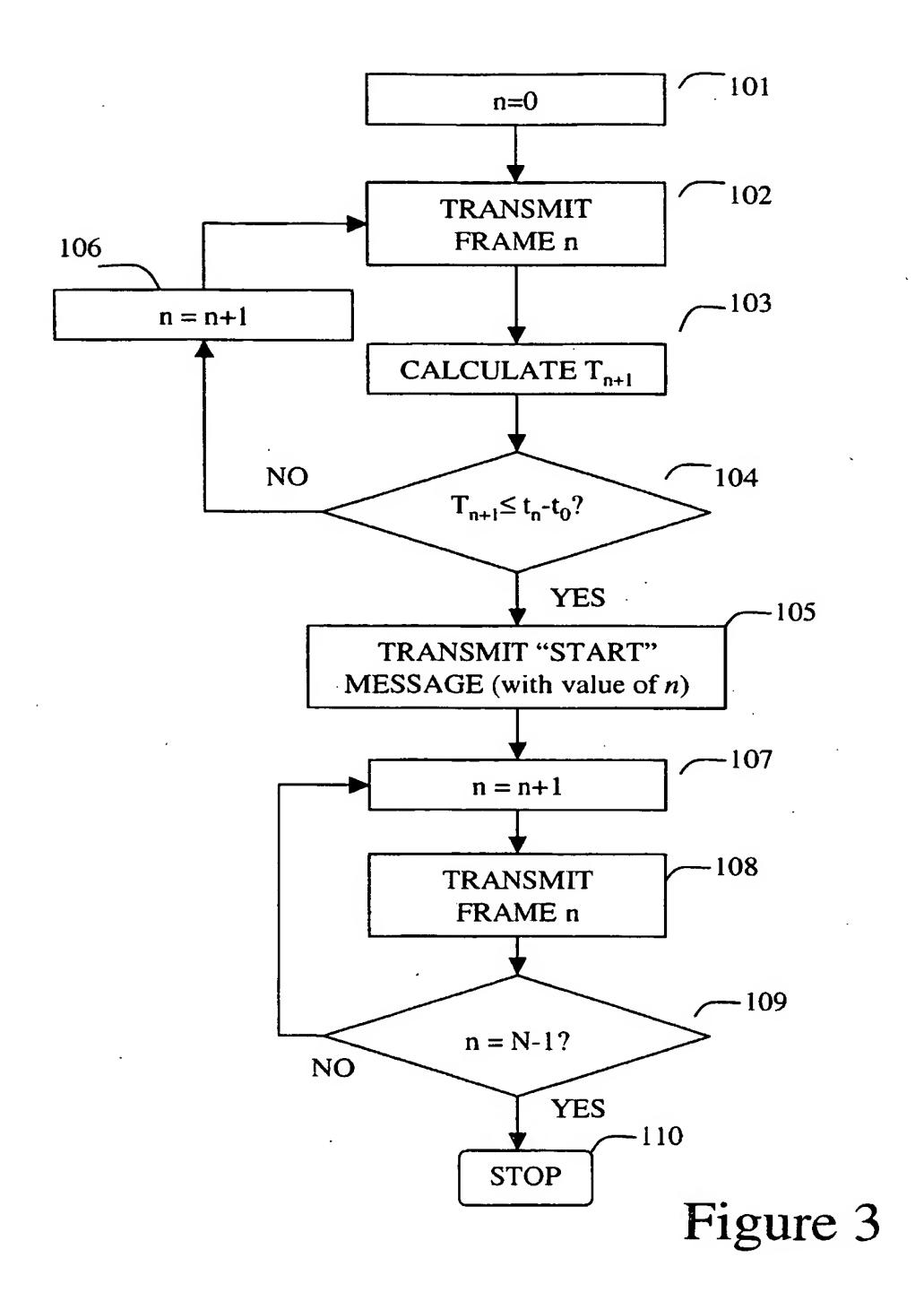
 t_0



b_i bits

Figure 2

b_{N-1} bits



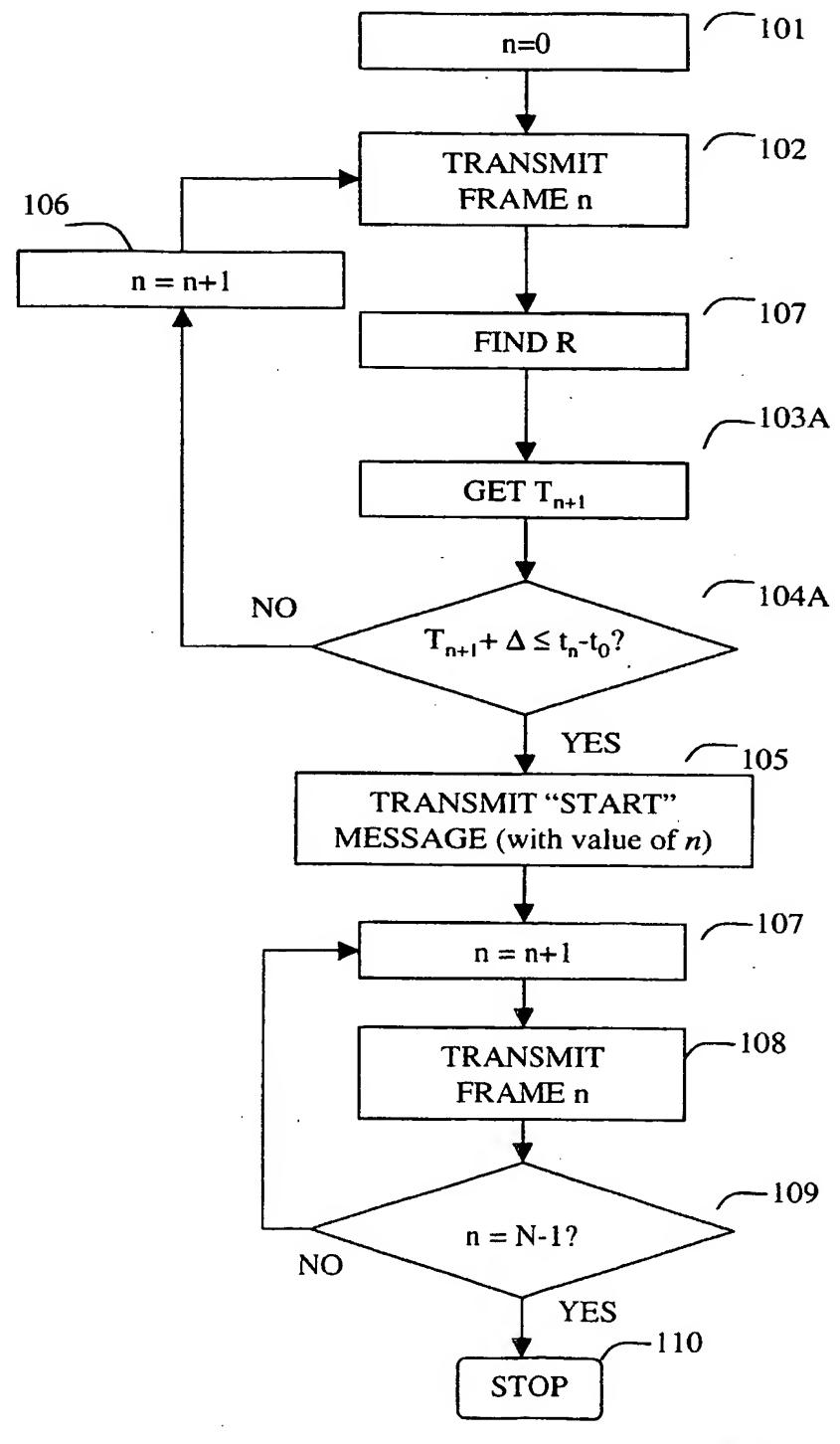


Figure 4

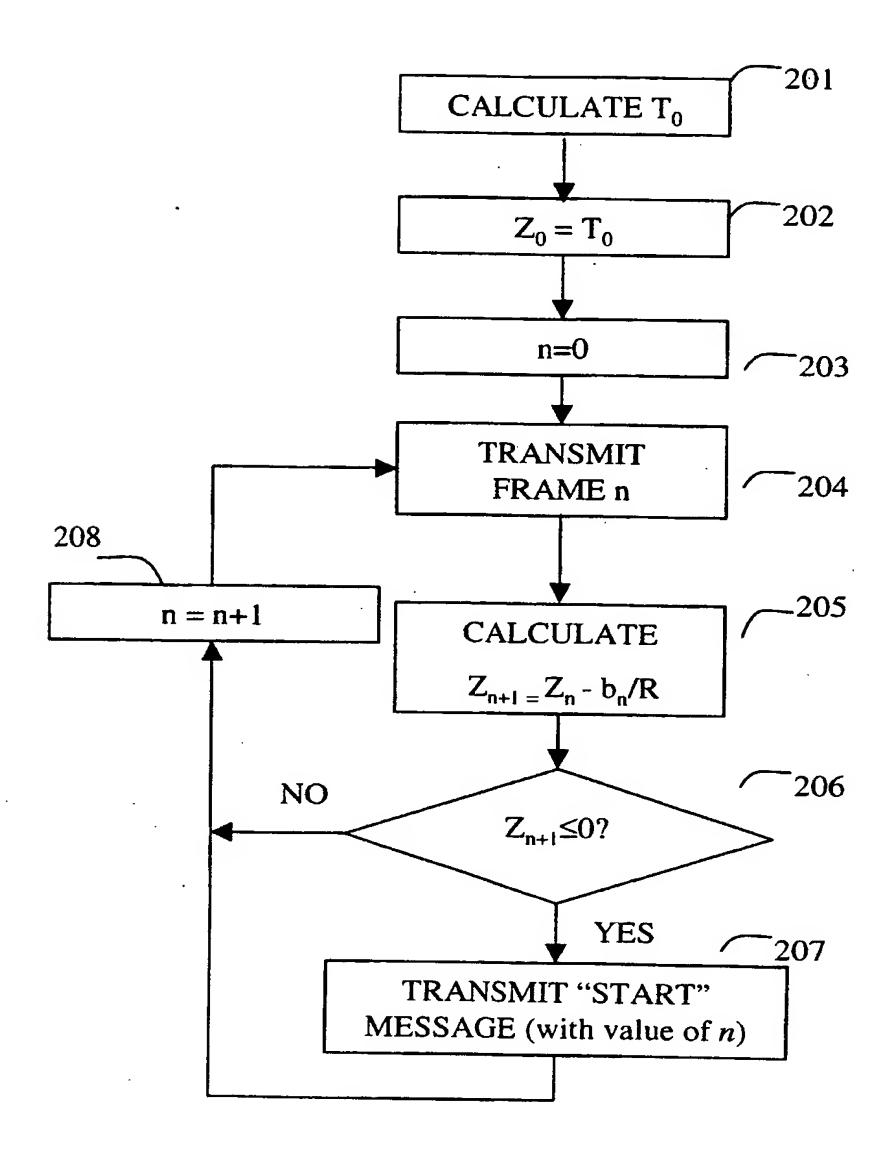


Figure 5